17581 U.S. 07/23/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF EXPRESS MAILING

dertify that this paper and the documents and/or fees referred to as attached therein are being deposited with the United States Postal Service on July 23, 2003 in an envelope as "Express Mail Post Office to Addressee" service under 37 CFR §1.10, Mailing Label Number ET582748330US, addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

- Gu Funcheda

Sue Funchess

10/62636

Attorney Docket No.: 03-0466/LSI1P225

First Named Inventor: LIM

UTILITY PATENT APPLICATION TRANSMITTAL (37 CFR. § 1.53(b))

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir: This is a request for filing a patent application under 37 CFR. § 1.53(b) in the name of inventors:

Hong Tee LIM, Wee Keong LIEW, and Chengyu GUO

For: INTEGRATED CIRCUIT PACKAGE AND METHOD FOR A PBGA PACKAGE HAVING A MULTIPLICITY OF STAGGERED POWER RING SEGMENTS FOR POWER CONNECTION TO INTEGRATED CIRCUIT DIE

Assigned to: LSI Logic Corporation

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Λ nn	lication	\vdash	emente
וטטבה	ucauon		lements:

\boxtimes	18 Pages of Specification, Claims and Abstract					
\boxtimes	5 Sheets of Informal Drawings					
\boxtimes	3 Pages Combined Declaration and Power of Attorney					
Accompany	ing Application Parts:					
	Do not publish this application. Nonpublication Request is attached.					
	Assignment and Assignment Recordation Cover Sheet					
	37 CFR 3.73(b) Statement by Assignee					
	Information Disclosure Statement with Form PTO-1449					
	Copies of IDS Citations					
	Preliminary Amendment					
\boxtimes	Return Receipt Postcard					
	Other:					

Claim For	Foreign Priority								
Pri	is claimed unde The certifi The certifi	ed copy is	c. § 119. enclosed. as been filed in prior app	plication U.S. A	pplication	No.			
Fee Calcul	lation (37 CFR § 1.	16)							
Applicant is entitled to Small Entity Status under 37 C.F.R. §1.27.									
	(Col. 1) Total Claims		(Col. 2) Claims	(Col. 3) Present Extra	Rate	Additional Fee			
TOTAL	20	MINUS	20	= 0	x 18 =	\$-0-			
INDEP.	3	MINUS	3	= 0	x 84 =	\$-0-			
	[]	First prese	entation of multiple dep	endent claim	\$280 =				
		Basic	Filing Fee under 37 C.	F.R. §1.16(a)	\$750 =	\$750.00			
				7	TOTAL	\$750.00			
SMALL ENTITY 50% FILING FEE REDUCTION (if applicable)									
			charge any fees which r	may be required	to Deposi	t Account			
General A	uthorization for Pet	ition for Ex	ktension of Time (37 Cl	FR §1.136)					
Appli for any sub	cants hereby make a	and general	lly authorize any Petitic sioner is also authorize unt No. 12-2252 (Order	ons for Extension d to charge any	extension	fees under 37 CFR			
Pleas	se send corresponde	nce to the	following address:						
Customer Number 24319 LSI Logic Corporation 1551 McCarthy Boulevard Milpitas, California 95035 Telephone (408) 433-6100 PATENT TRADEMARK OFFICE									
Date: Jul	y 23, 2003		Francis T. Kalinski Registration No. 44		1				

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: LIM Attorney Docket No.: 03-0466/LSI1P225

Application No.: Filed Herewith Examiner: Unassigned

Filed: Herewith Group: Unassigned

Title: INTEGRATED CIRCUIT PACKAGE AND METHOD FOR A PBGA PACKAGE HAVING A MULTIPLICITY OF STAGGERED POWER RING SEGMENTS FOR POWER CONNECTION TO INTEGRATED CIRCUIT

DIE

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Sue Funchess

NONPUBLICATION REQUEST AND CERTIFICATION (35 U.S.C. 122(b)(2)(B)(i))

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen (18) months after filing. I hereby request that the attached application **NOT** be published under 35 U.S.C. 122(b).

Date: <u>July 23, 2003</u>

Francis T. Kalinski II Registration No. 44,177